



AF/2827

**BOX AF**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of: **AKAMATSU, et al**

Group Art Unit: **2827**

Serial No.: **09/392,722**

Examiner: **D. E. GRAYBILL**

Filed: **September 9, 1999**

P.T.O. Confirmation No.: 1679

**FOR: INTEGRATED ELECTRONIC DEVICE HAVING FLIP-CHIP CONNECTION  
WITH CIRCUIT BOARD AND FABRICATION METHOD THEREOF**

**AMENDMENT AFTER FINAL REJECTION**

**BOX AF**

Commissioner for Patents  
Washington, D.C. 20231

January 8, 2003

Sir:

In response to the Office Action dated October 8, 2002, please amend the above-identified application as follows:

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JAN 13 2003  
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